



RAYSTAR

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RG240128A-TIF-V

SPECIFICATION

CUSTOMER : _____

<p>APPROVED BY: (FOR CUSTOMER USE ONLY)</p>	<p>PCB VERSION: DATA:</p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
ISSUED DATE:			

Contents

1.	<i>Specification Revision History</i>	<i>Page 3</i>
2.	<i>General Specification</i>	<i>Page 4</i>
3.	<i>Module Classification Information</i>	<i>Page 5</i>
4.	<i>Interface Pin Function</i>	<i>Page 6</i>
5.	<i>Outline dimension & Block Diagram</i>	<i>Page 7</i>
6.	<i>Display Control Instruction</i>	<i>Page 8</i>
7.	<i>Timing Characteristics</i>	<i>Page 33</i>
8.	<i>Optical Characteristics</i>	<i>Page 34</i>
9.	<i>Absolute Maximum Ratings</i>	<i>Page 35</i>
10.	<i>Electrical Characteristics</i>	<i>Page 35</i>
11.	<i>Backlight Information</i>	<i>Page 36</i>
12.	<i>Reliability</i>	<i>Page 37</i>
13.	<i>Inspection specification</i>	<i>Page 38</i>
14.	<i>Precautions in use of LCD Modules</i>	<i>Page 42</i>

1. Specification Revision History

RECORDS OF REVISION			
VERSION	DATE	REVISED PAGE NO.	Note
1	2007.11.07		First issue

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2. General Specification

The Features of the Module is description as follow:

- Module dimension: 170.0 x 103.5 x 14.0(max.) mm³
- View area: 128.0 x 75.0 mm²
- Active area: 119.97 x 63.97 mm²
- Number of Dots: 240 x 128
- Dot size: 0.47 x 0.47 mm²
- Dot pitch: 0.5 x 0.5 mm²
- LCD type: FSTN Negative Transmissive
- Duty: 1/128
- View direction: 6 o'clock
- Backlight Type: CCFL White

3. Module Classification Information

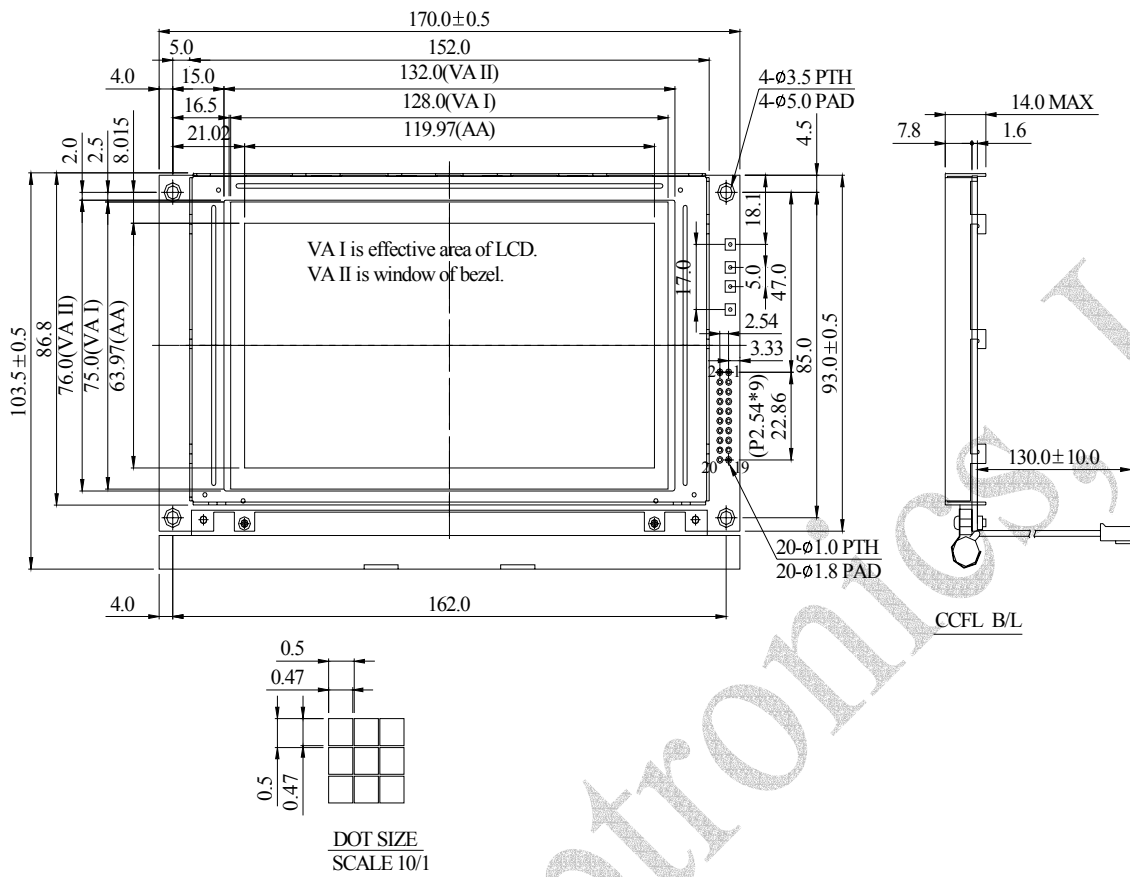
R G 240128 A—T I F— V
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧

Item	Description		
1	Brand : Raystar Optronics Inc.		
2	Display Type	C : Character Type,	
		G : Graphic Type	
3	Display Font : 240 x128 Dots		
4	Serials Code.		
5	LCD Mode	P : TN Positive, Gray	
		N : TN Negative,	
		G : STN Positive, Gray	
		Y : STN Positive, Yellow Green	
		B : STN Negative, Blue	
		F : FSTN Positive	
6	LCD Polarizer Type/ Temperature range/ View direction	A : Reflective, N.T, 6:00	K : Transflective, W.T,12:00
		D : Reflective, N.T, 12:00	1 : Transflective, U.T,6:00
		G : Reflective, W. T, 6:00	4 : Transflective, U.T.12:00
		J : Reflective, W. T, 12:00	C : Transmissive, N.T,6:00
		0 : Reflective, U. T, 6:00	F : Transmissive, N.T,12:00
		3 : Reflective, U. T, 12:00	I : Transmissive, W. T, 6:00
		B : Transflective, N.T,6:00	L : Transmissive, W.T,12:00
		E : Transflective, N.T.12:00	2 : Transmissive, U. T, 6:00
		H : Transflective, W.T,6:00	5 : Transmissive, U.T,12:00
7	Backlight Type	N : Without backlight	Y : LED, Yellow Green
		P : EL, Blue green	A : LED, Amber
		T : EL, Green	W : LED, White
		D : EL, White	O : LED, Orange
		F : CCFL, White	G : LED, Green
8	Special Code	V: With Negative voltage output	

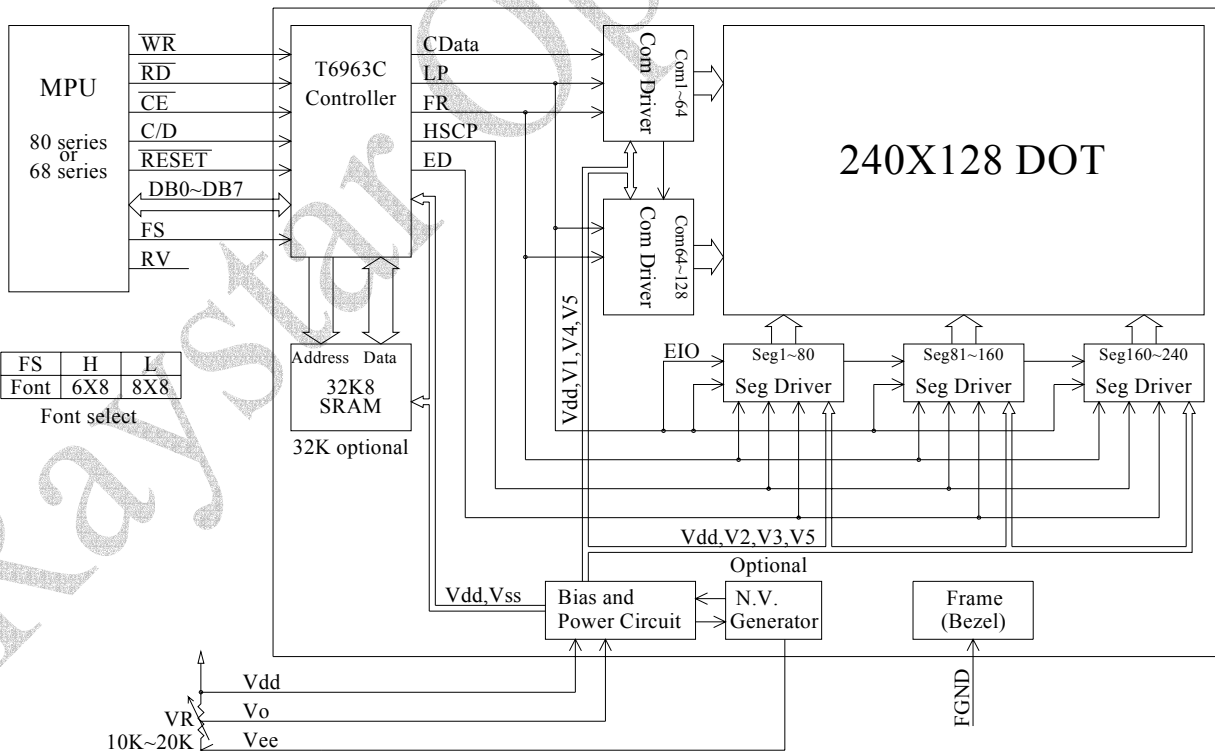
4. Interface Pin Function

Pin No.	Symbol	Level	Description
1	FG	—	Frame ground (Connected to bezel)
2	V _{ss}	—	GND
3	V _{dd}	—	Power supply (+5 V)
4	V _o	—	Power supply for LCD driver
5	WR	L	Data write. Write data into T6963C when WR = L
6	RD	L	Data read. Read data from T6963C when RD = L
7	CE	L	L : Chip enable
8	C/D	H / L	WR=L , C/D=H : Command Write C/D=L: Data write RD=L , C/D=H : Status Read C/D=L: Data read
9	V _{ee}	—	Negative voltage
10	RESET	H / L	H : Normal ; L : Initialize T6963C
11	DB0	H / L	Data bus line
12	DB1	H / L	Data bus line
13	DB2	H / L	Data bus line
14	DB3	H / L	Data bus line
15	DB4	H / L	Data bus line
16	DB5	H / L	Data bus line
17	DB6	H / L	Data bus line
18	DB7	H / L	Data bus line
19	FS	MD2	Pins for selection of font; H : 6 * 8 , L : 8 * 8
20	RV	H / L	H:Reverse H:Normal

5. Outline Dimension & Block Diagram



PIN NO.	SYMBOL
1	FGND
2	GND
3	Vdd
4	Vo
5	WR
6	RD
7	CE
8	C/D
9	Vee
10	RESET
11	DB0
12	DB1
13	DB2
14	DB3
15	DB4
16	DB5
17	DB6
18	DB7
19	FS
20	RV



External contrast adjustment.

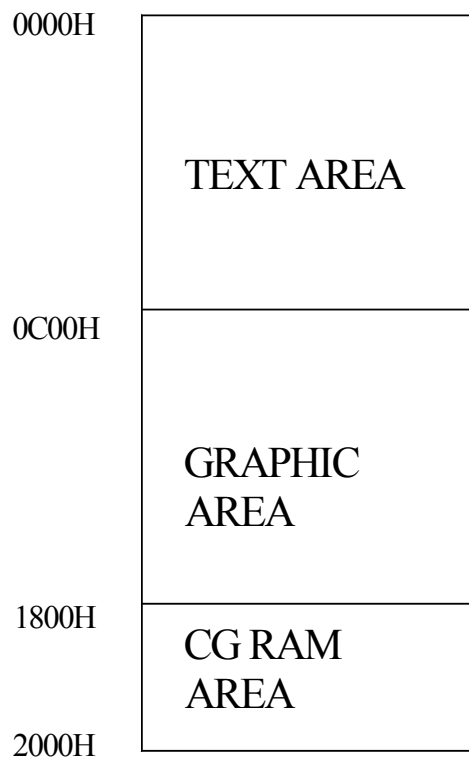
6. Display Control Instruction

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM (refer to Table 1.), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

- RAM Interface

The external RAM is used to store display data(text, graphic and external CG data). It can be freely allocated to the memory area(8 Kbyte max).

Recommend



• Flowchart of communications with MPU

(1) Status Read

A status check must be performed before data is read or written.

Status check

The Status of T6963C can be read from the data lines.

\overline{RD}	L
\overline{WR}	H
\overline{CE}	L
C/D	H
Do to D7	H

The T6963C status word format is as follows:

MSB				LSB			
STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

STA0	Check command execution capability	0:Disable 1:Enable
STA1	Check data read/write Capability	0:Disable 1:Enable
STA2	Check Auto mode data read capability	0:Disable 1:Enable
STA3	Check Auto mode data write capability	0:Disable 1:Enable
STA4	Not used	
STA5	Check controller operation capability	0:Disable 1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy commands.	0:No error 1:Error
STA7	Check the blink condition	0:Disable off 1:Normal display

(Note 1) It is necessary to check STA0 and STA1 at the same time.

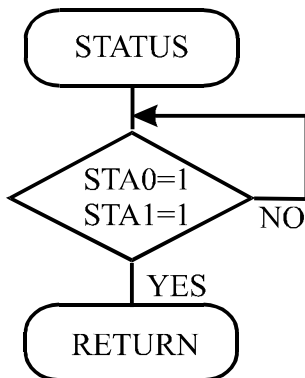
There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

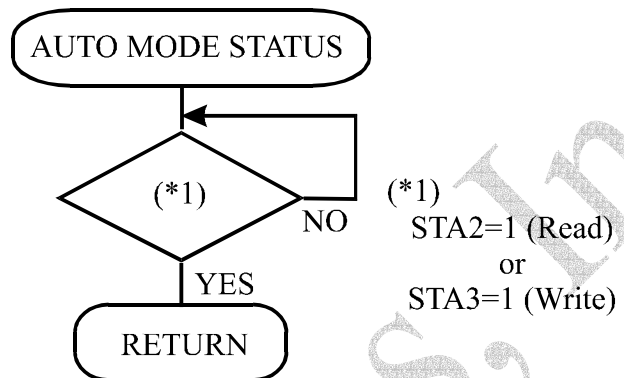
(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

Status Checking flow

(a)



(b)



(Note 4) When using the MSB=0 command, a Status Read must be performed.

If a status check is not carried out, the T6963C cannot operate normally, even after a delay time.

The hardware interrupt occurs during the address calculation period (at the end of each line).

If a MSB=0 command is sent to the T6963C during this period, the T6963C enters Wait status.

If a status check is not carried out in this state before the next command is sent, there is the possibility that the command or data date will not be received.

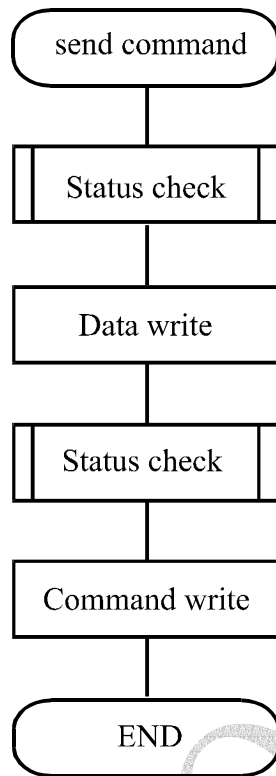
(2)Setting date

When using the T6963C, first set the data, then set the command.

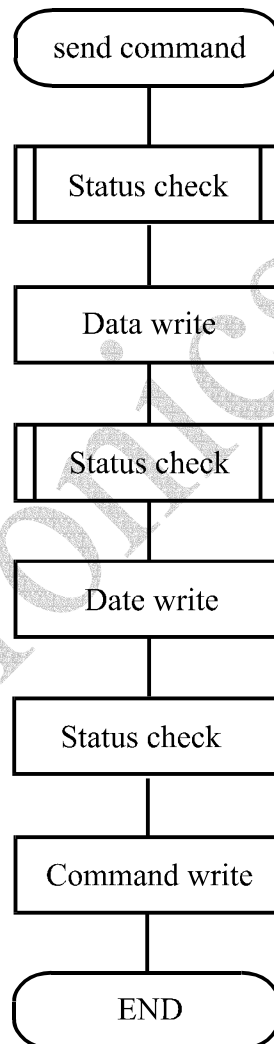
Procedure for sending a command

(a)The case of 1 date

(b)The case of 2 data



STA0, 1



STA0, 1

(Note) When sending more than two data, the last datum (or last two data)is valid.

• COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001 00100010 00100100	X address Date Low address	Y address 00H High address	Set Cursor Pointer Set Offset Register Set Address Pointer
SET CONTROL WORD	01000000 01000001 01000010 01000011	Low address Columns Low address Columns	High address 00H High address 00H	Set Text Home Address Set Text Area Set Graphic Home Address Set Graphic Area
MODE SET	1000x000 1000x001 1000x011 1000x100 10000xxx 10001xxx	— — — — — —	— — — — — —	OR mode EXOR mode AND mode Text Attribute mode Internal CG ROM mode External CG RAM mode
DISPLAY MODE	10010000 1001xx10 1001xx11 100101xx 100110xx 100111xx	— — — — — —	— — — — — —	Display off Cursor on, blink off Cursor on, blink on Text on, graphic off Text off, graphic on Text on, graphic on
CURSOR PATTERN SELECT	10100000 10100001 10100010 10100011 10100100 10100101 10100110 10100111	— — — — — — — —	— — — — — — — —	1-line cursor 2-line cursor 3-line cursor 4-line cursor 5-line cursor 6-line cursor 7-line cursor 8-line cursor
DATA AUTO READ/WRITE	10110000 10110001 10110010	— — —	— — —	Set Data Auto Write Set Data Auto Read Auto Reset
DATA READ/WRITE	11000000 11000001 11000010 11000011 11000100 11000101	Data — Data — Data —	— — — — — —	Data Write and Increment ADP Data Read and Increment ADP Data Write and Decrement ADP Data Read and Decrement ADP Data Write and Non-variable ADP Data Read and Non-variable ADP
SCREEN PEEK	11100000	—	—	Screen Peek
SCREEN COPY	11101000			Screen Copy

BIT SET/RESET	11110xxx	—	—	Bit Reset
	11111xxx	—	—	Bit Set
	1111x000	—	—	Bit 0 (LSB)
	1111x001	—	—	Bit 1
	1111x010	—	—	Bit 2
	1111x011	—	—	Bit 3
	1111x100	—	—	Bit 4
	1111x101	—	—	Bit 5
	1111x110	—	—	Bit 6
	1111x111	—	—	Bit 7 (MSB)

X : invalid

• Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	Y ADRS
00100010	22H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1)Set Cursor Pointer

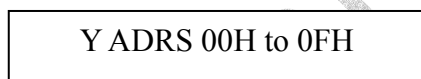
The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only be moved by this command. Data read/write from the MPU never changes the cursor pointer. X ADRS and Y ADRS are specified as follows.

X ADRS 00H to 4FH (lower 7 bits are valid)

Y ADRS 00H to 1FH (lower 5 bits are valid)

Single-Scan

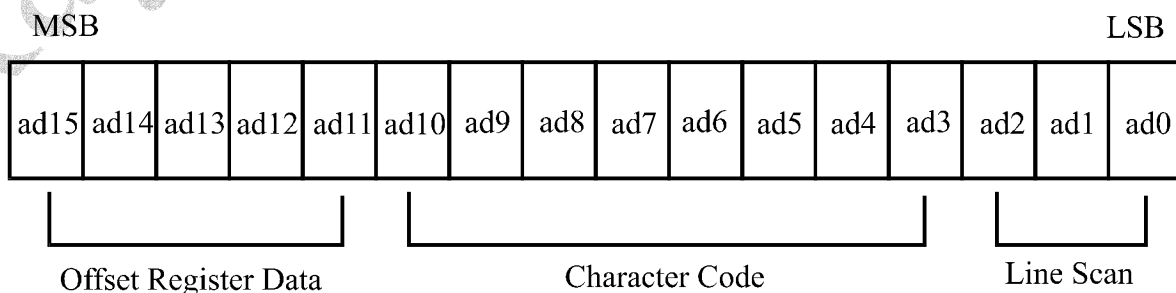
X ADRS 00 to 4FH



(2)Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.



T6963C assign External character generator, when character code set 80H TO FFH in using internal character generator. Character code 00H to 80H assign External character generator, when External generator mode.

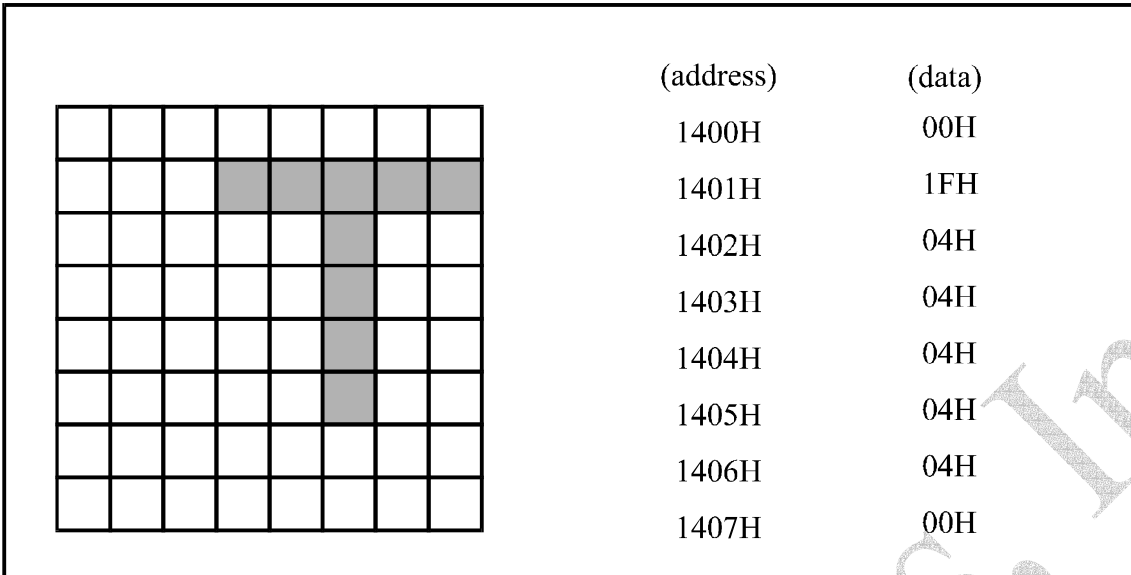
The senior five bits define the start address in external memory of the CG RAM area. The next eight bits represent the character code of the character. In internal CG ROM, character codes 00H to 7FH represent the predefined “internal” CG ROM characters, and codes 80H to FFH represent the user’s own “external” characters. In external CG ROM mode, all 256 codes from 00H to FFH can be used to represent the user’s own characters. The three least significant bits indicate one of the eight rows of eight dots that define the character’s shape.

The relationship between display RAM address and offset register

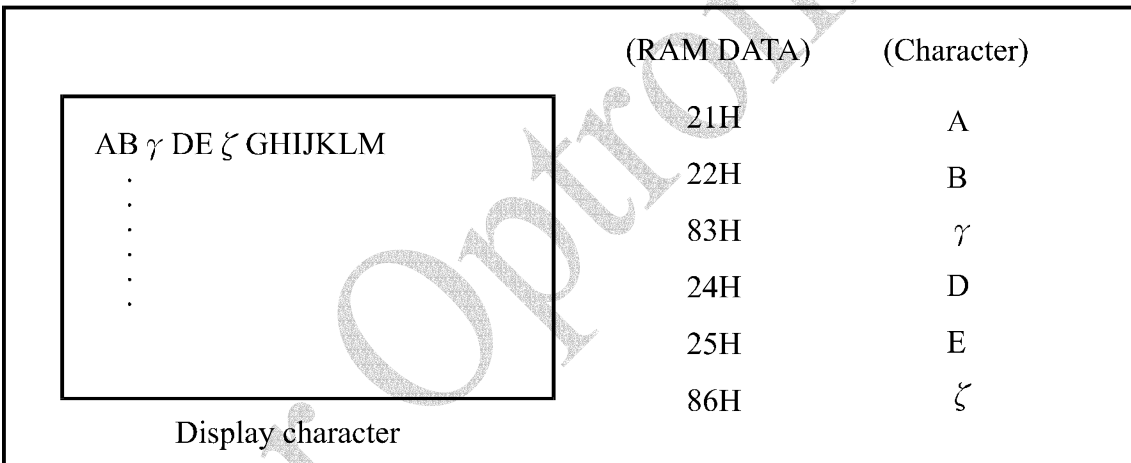
Offset register data	CG RAM hex. address (start to end)
00000	0000 to 07 FFH
00001	0800 to 0FFFH
00010	1000 to 17FFH
11100	E000 to E7FFH
11101	E800 to EFFFH
11110	F000 to F7FFH
11111	F800 to FFFFH

(Example 1)

Offset register	02H
Character code	80H
Character generator RAM start address	0001 0100 0000 0000
	1 4 0 0 H



(Example 2) The relationship between display RAM data and display characters

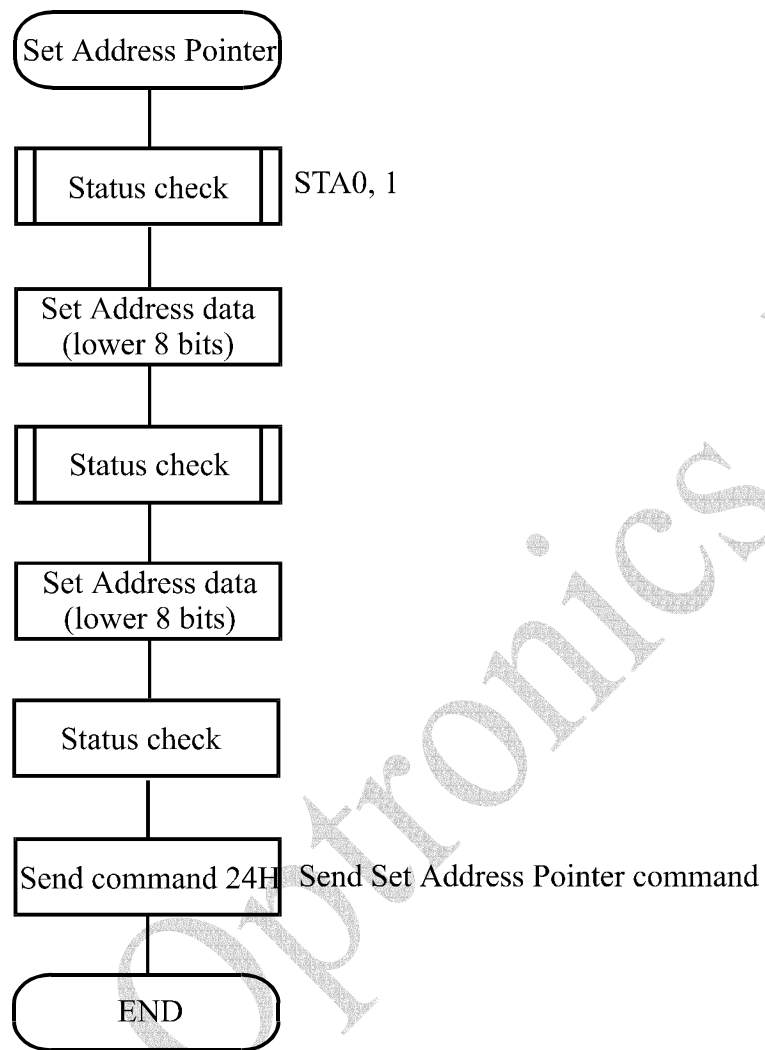


γ and ζ are displayed by character generator RAM.

(3) Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from) external RAM.

The Flowchart for Set Address Pointer command



• Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1)Set Text Home Address

The starting address in the external display RAM for text display is defined by this command.

The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH		TH+CL
TH+TA		TH+TA+CL
(TH+TA)+TA		TH+2TA+CL
(TH+2TA)+TA		TH+3TA+CL
TH+(n-1)TA		TH+(n-1)TA+CL

TH: Text home address

TA: Text area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Text home address : 0000H
 Text area : 0020H
 : 32 Columns
 : 4 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	002FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

(2)Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this command. The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH		GH+GL
GH+GA		GH+GA+CL
(GH+GA)+GA		GH+2GA+CL
(GH+2GA)+GA		GH+3GA+CL
GH+(n-1)GA		GH+(n-1)GA+CL

GH: Graphic home address

GA: Graphic area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Graphic home address : 0000H
Graphic area : 0020H
: 32 Columns
: 2 Lines

If the graphic area setting is set to match the desired number of columns on the LCD, the addressing scheme will be automatically modified so that the start address of each line equals the end address of the previous line +1.

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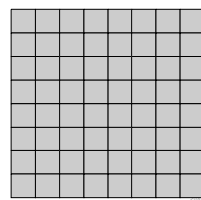
• Mode set

CODE	FUNCTION	OPERAND
1000x000	OR Mode	—
1000x001	EXOR Mode	—
1000x011	AND Mode	—
1000x100	TEXT ATTRIBUTE Mode	—
10000xxx	Internal Character Generator Mode	—
10001xxx	External Character Generator Mode	—

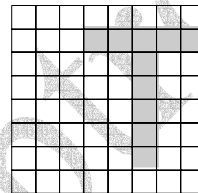
X: invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

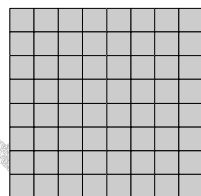
(Example)



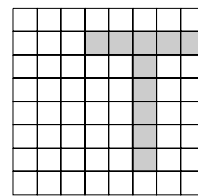
GRAPHIC



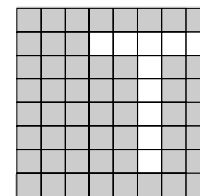
TEXT



“OR”



“AND”



“TXOR”

(Note) Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

The attribute data for each character in the text area is written to the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1byte

×	×	×	×	d3	d2	d1	d0
---	---	---	---	----	----	----	----

d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

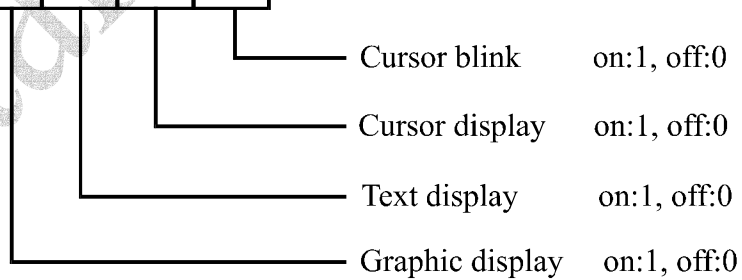
X: invalid

• Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	—
1001xx10	Cursor on, blink off	—
1001xx11	Cursor on, blink on	—
100101xx	Text on, graphic off	—
100110xx	Text off, graphic on	—
100111xx	Text on, graphic on	—

X: invalid

1	0	0	1	D3	D2	D1	D0
---	---	---	---	----	----	----	----



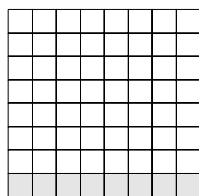
(Note) It is necessary to turn on “Text display” and “Graphic display” in the following cases.

- a) Combination of text/graphic display
- b) Attribute function

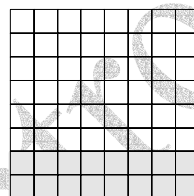
• Cursor pattern select

CODE	FUNCTION	OPERAND
10100000	1-line cursor	—
10100001	2-line cursor	—
10100010	3-line cursor	—
10100011	4-line cursor	—
10100100	5-line cursor	—
10100101	6-line cursor	—
10100110	7-line cursor	—
10100111	8-line cursor	—

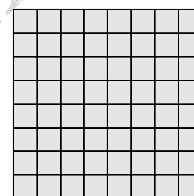
When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines. The cursor address is defined by the Cursor Pointer Set command.



1-line cursor



2-line cursor



8-line cursor

• Data Auto Read/Write

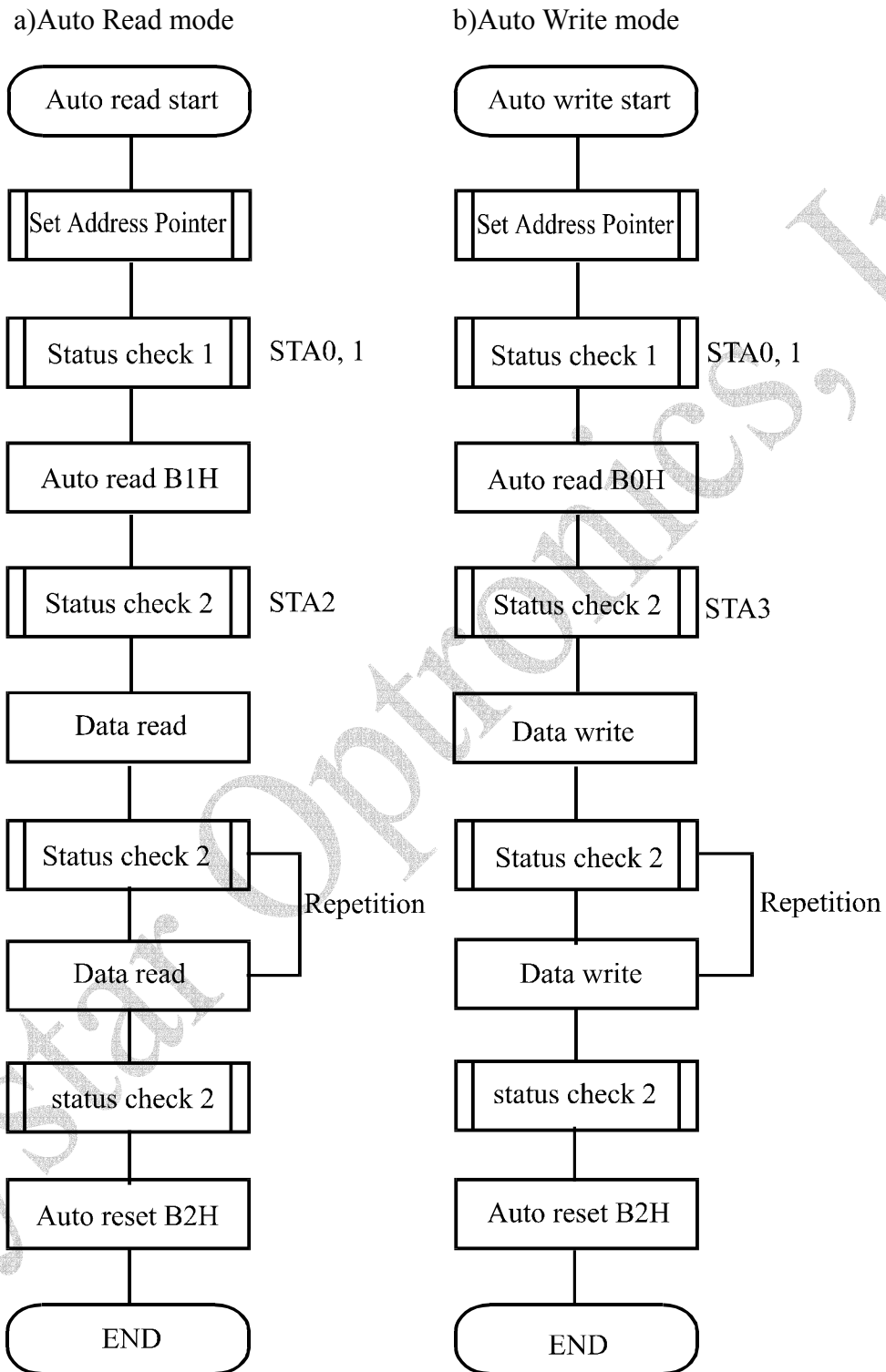
CODE	HEX.	FUNCTION	OPERAND
10110000	B0H	Set Data Auto Write	—
10110001	B1H	Set Data Auto Read	—
10110010	B2H	Auto Reset	—

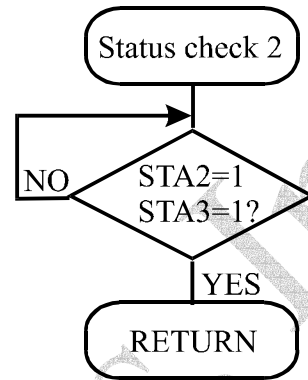
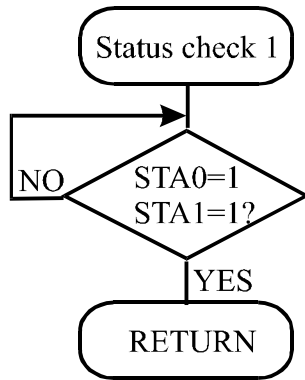
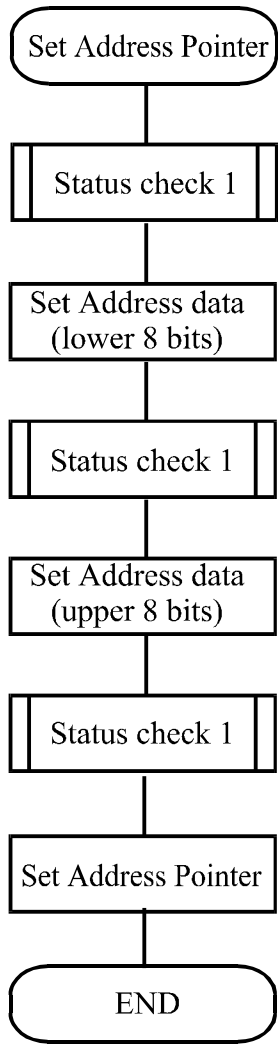
The command is convenient for sending a full screen of data from the external display RAM. After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command. After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode.

(Note)A Status check for Auto mode

(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.





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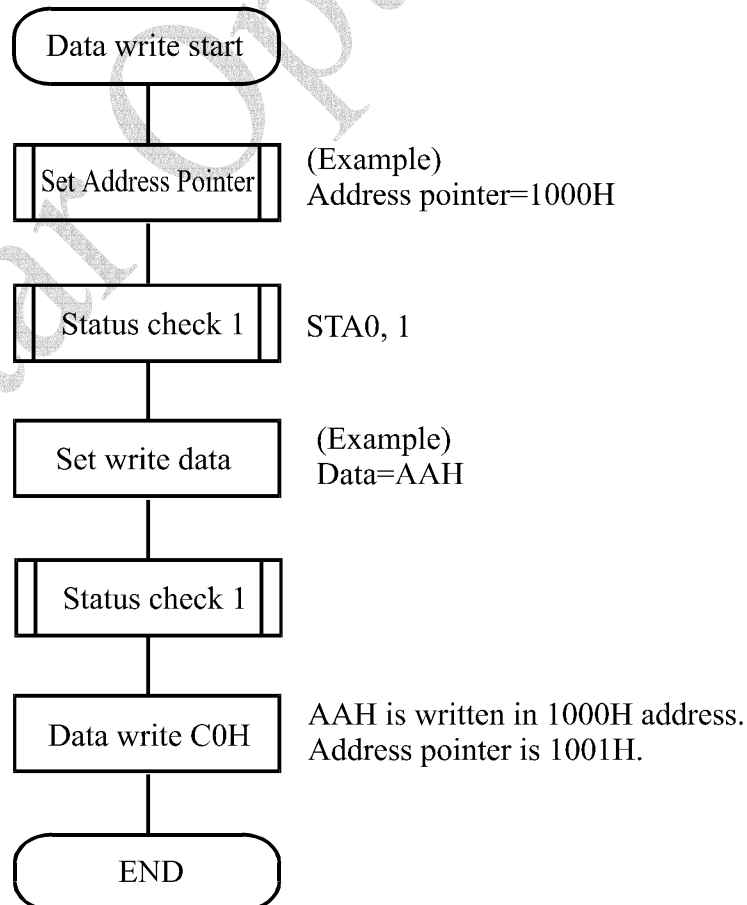
• Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	C0H	Data Write and Increment ADP	Data
11000001	C1H	Data Read and Increment ADP	—
11000010	C2H	Data Write and Decrement ADP	Data
11000011	C3H	Data Read and Decrement ADP	—
11000100	C4H	Data Write and Non-variable ADP	Data
11000101	C5H	Data Read and Non-variable ADP	—

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note) This command is necessary for each 1-byte datum.

Refer to the following flowchart.



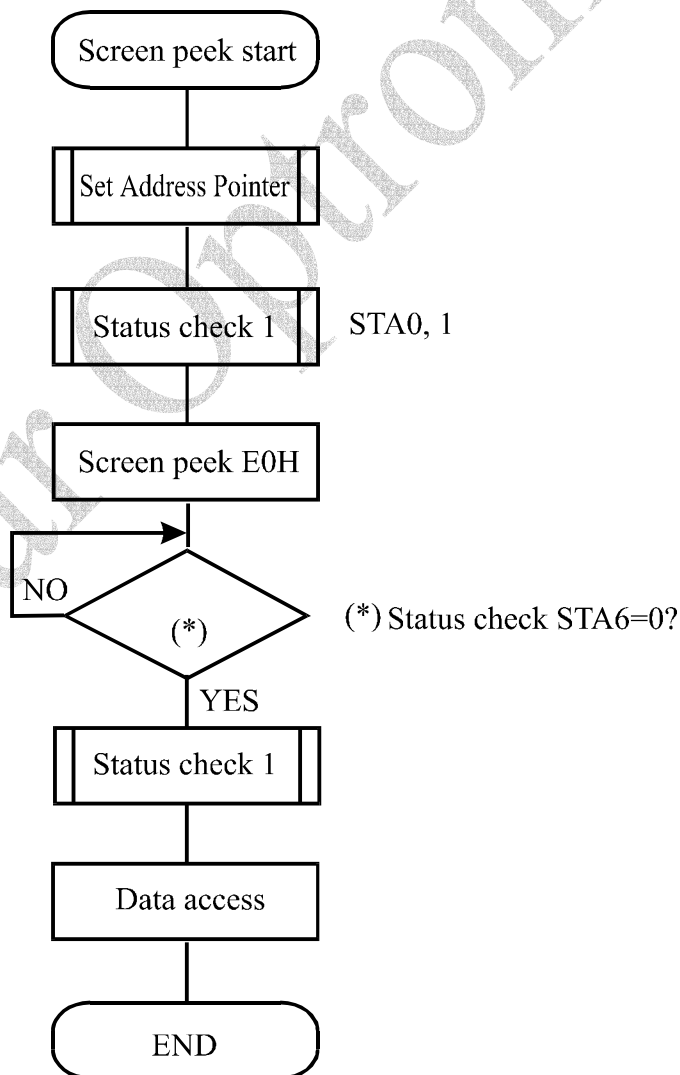
• Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	E0H	Screen Peek	—e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this command is ignored and a status flag (STA6) is set.

Refer to the following flowchart.



• Screen Copy

CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	—

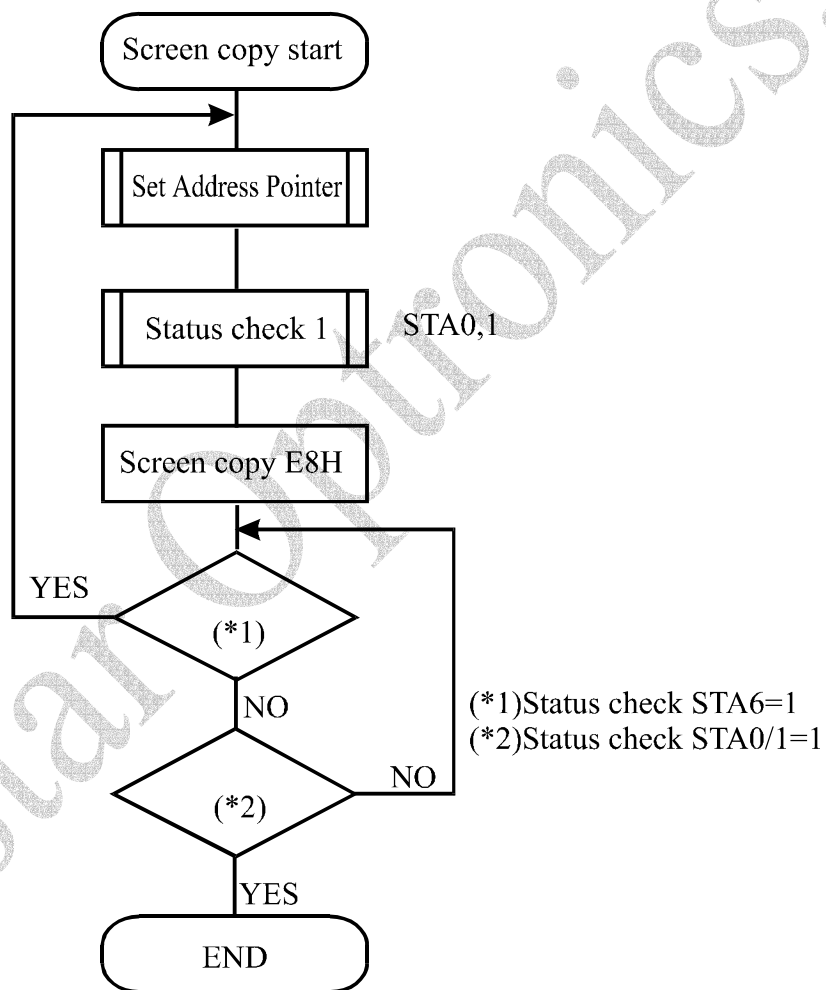
This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1) If the attribute function is being used, this command is not available.

(With Attribute data is graphic area data.)

Refer to the following flowchart.



• Bit Set/Reset

CODE	FUNCTION	OPERAND
11110xxx	Bit Reset	—
11111xxx	Bit Set	—
1111x000	Bit 0 (LSB)	—
1111x001	Bit 1	—
1111x010	Bit 2	—
1111x011	Bit 3	—
1111x100	Bit 4	—
1111x101	Bit 5	—
1111x110	Bit 6	—
1111x111	Bit 7 (MSB)	—

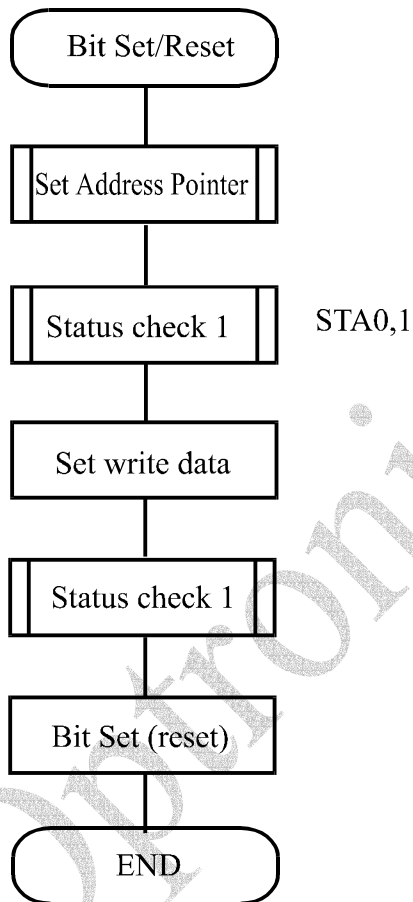
X: invalid

Raystar Opttronics, Inc.

This command use to set or reset a bit of the byte specified by the address pointer.

Only one bit can be set/reset at a time.

Refer to the following flowchart.



CHARACTER CODE MAP

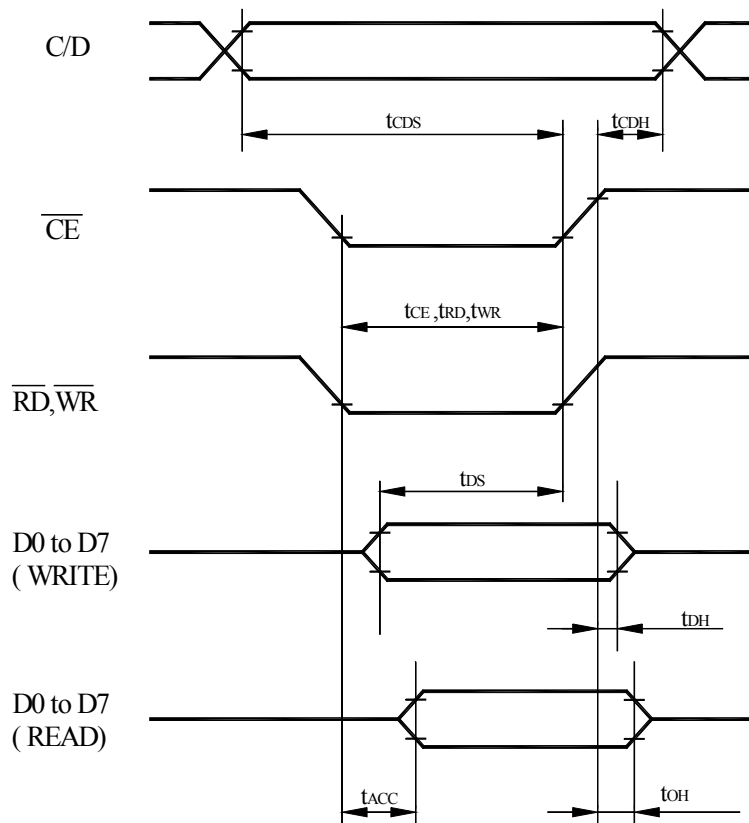
Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH
LLLL		0	1	2	3	4	5	6
LLLH	.	7	8	9	A	B	C	D
LLHL	"	E	F	G	H	I	J	K
LLHH	#	L	M	N	O	P	Q	R
LHLL	\$	S	T	U	V	W	X	Y
LHLH	%	Z	[\]	^	_	`
LHHL	&	a	b	c	d	e	f	g
LHHH	'	h	i	j	k	l	m	n
HLLL	o	p	q	r	s	t	u	v
HLLH	w	x	y	z	{		}	~
HLHL	*	?	@	A	B	C	D	E
HLHH	F	G	H	I	J	K	L	M
HHLL	N	O	P	Q	R	S	T	U
HHLH	V	W	X	Y	Z	[\]
HHHL	^	_	`	a	b	c	d	e
HHHH	f	g	h	i	j	k	l	m

7. Timing Characteristics

· Busy Timing

($V_{SS} = 0\text{ V}$, $V_{DD} = 5\text{ V}$)

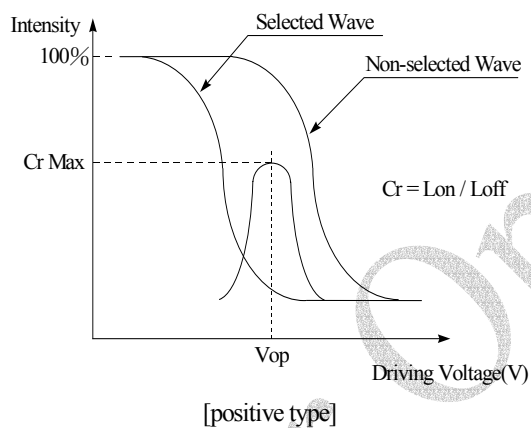
Item	Symbol	Min	Typ	Max	Unit
C/D Set-up Time	t _{CDS}	100	—	—	ns
C/D Hold Time	t _{CDH}	10	—	—	ns
CE,RD,WR Pulse Width	t _{CDS,tRD,tWR}	80	—	—	ns
Data Set-up Time	t _{DS}	80	—	—	ns
Data Hold Time	t _{DH}	40	—	—	ns
Access Time	t _{ACC}	—	—	150	ns
Output Hold Time	t _{OH}	10	—	50	ns



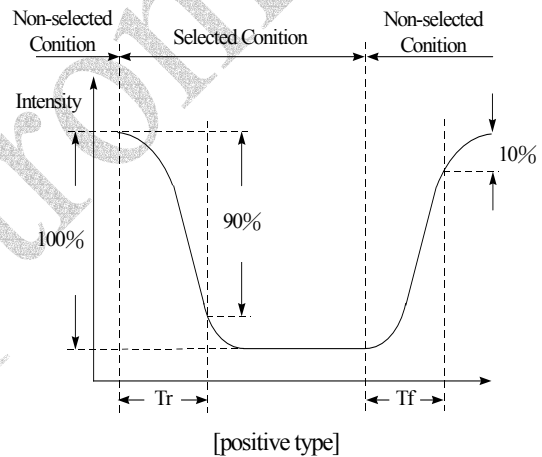
8. Optical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
View Angle	(V) θ	$CR \geq 2$	30	—	60	deg
	(H) φ	$CR \geq 2$	-45	—	45	deg
Contrast Ratio	CR	—	—	5	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	200	300	ms

Definition of Operation Voltage (Vop)



Definition of Response Time (Tr, Tf)



Conditions :

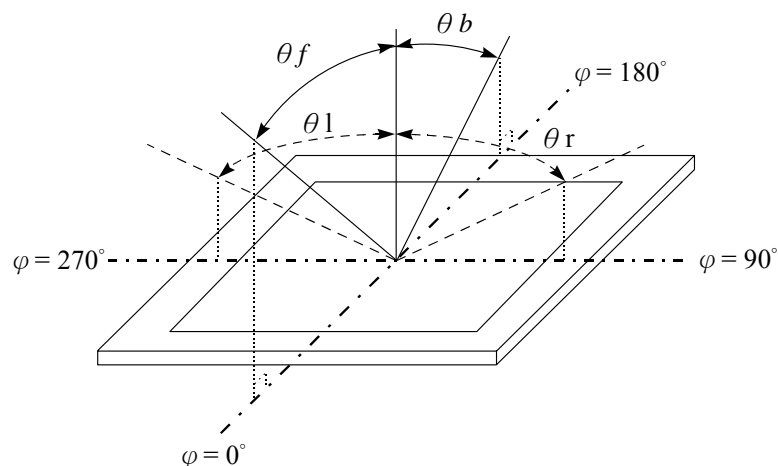
Operating Voltage : Vop

Viewing Angle(θ , φ) : 0° , 0°

Frame Frequency : 64 HZ

Driving Waveform : 1/N duty , 1/a bias

Definition of viewing angle($CR \geq 2$)



9. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	°C
Storage Temperature	T_{ST}	-30	—	+80	°C
Input Voltage	V_I	V_{SS}	—	V_{DD}	V
Supply Voltage For Logic	$V_{DD}-V_{SS}$	-0.3	—	+7	V
Supply Voltage For LCD	$V_{DD}-V_0$	0	—	27	V
Negative Voltage Output	V_{EE}	—	—	-22	V

10. Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	4.75	5.0	5.25	V
Supply Voltage For LCD	$V_{DD}-V_0$	$T_a=-20^{\circ}\text{C}$	—	—	20.1	V
		$T_a=25^{\circ}\text{C}$	—	18.0	—	V
		$T_a=70^{\circ}\text{C}$	16.3	—	—	V
Input High Volt.	V_{IH}	—	$V_{DD}-2.2$	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	0	—	0.8	V
Output High Volt.	V_{OH}	—	$V_{DD}-0.3$	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	0	—	0.3	V
Supply Current	I_{DD}	$V_{DD}=5\text{V}$	15	23	35	mA

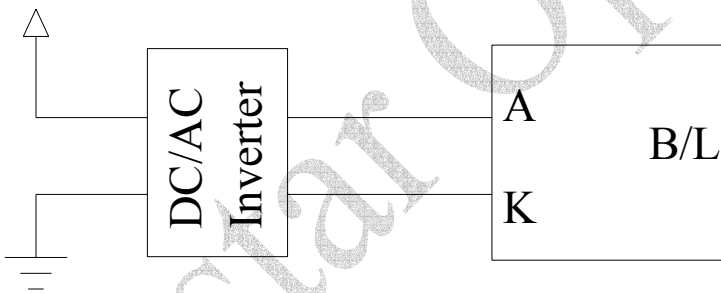
11. Backlight Information

CCFL backlight Specification

(Ta=25°C)

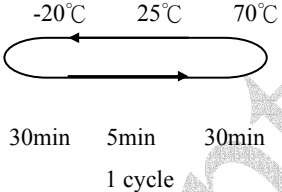
Item	Symbol	Specification			Unit	Condition
		Min	Typ	Max		
Driving Voltage	V _{FL}	—	385	—	Vrms	—
Input current	I _{FL}	4.5	5.0	55	mArms	—
Luminance	L	—	900	—	Cd/m ²	$\varphi, \theta = 0 \text{ deg}, I_{FL} = 5.0 \text{ mArms}$
Chromaticity	x	—	0.320	—	—	—
	y	—	0.340	—	—	—
Luminance Uniformity (Testing 9 point)	—	75%	—	—	%	$\varphi, \theta = 0 \text{ deg}, I_{FL} = 5.0 \text{ mArms}$
Life time	—	17000	—	—	hrs	

CCFL B/L drives directly from A , K.



12. Reliability

Content of Reliability Test (wide temperature, -20°C~70°C)

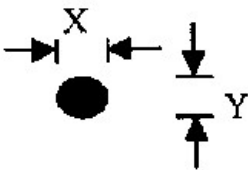
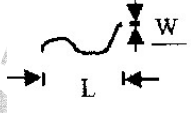
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation 	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 15mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	—

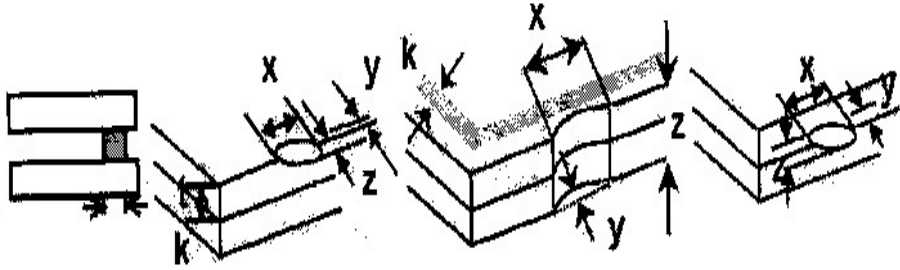
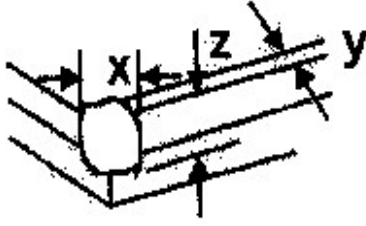
Note1: No dew condensation to be observed.

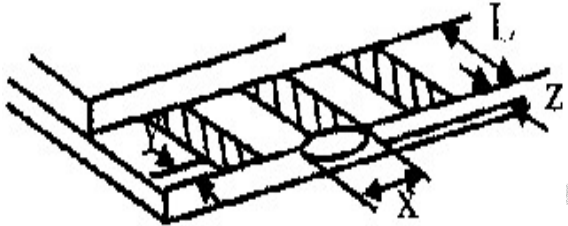
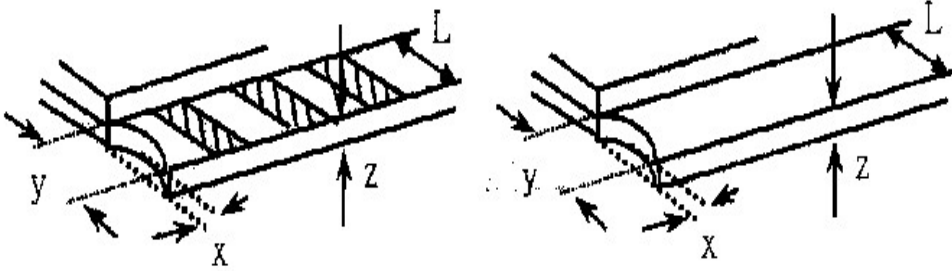
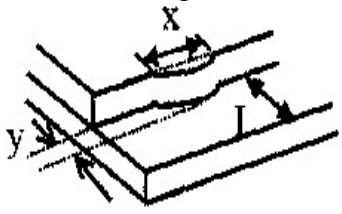
Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

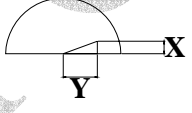
Note3: Vibration test will be conducted to the product itself without putting it in a container.

13. Inspection specification

NO	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65												
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leq 0.25\text{mm}$, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5												
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi = (x + y) / 2$  <table border="1" data-bbox="874 936 1353 1187"> <thead> <tr> <th>SIZE</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>2</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$0.25 < \Phi$</td> <td>0</td> </tr> </tbody> </table>	SIZE	Acceptable Q TY	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	1	$0.25 < \Phi$	0	2.5		
		SIZE	Acceptable Q TY												
$\Phi \leq 0.10$	Accept no dense														
$0.10 < \Phi \leq 0.20$	2														
$0.20 < \Phi \leq 0.25$	1														
$0.25 < \Phi$	0														
3.2 Line type : (As following drawing)  <table border="1" data-bbox="710 1249 1353 1460"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.02$</td> <td>Accept no dense</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.02 < W \leq 0.03$</td> <td rowspan="2">2</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.03 < W \leq 0.05$</td> </tr> <tr> <td>---</td> <td>$0.05 < W$</td> <td>As round type</td> </tr> </tbody> </table>	Length	Width	Acceptable Q TY	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	$L \leq 2.5$	$0.03 < W \leq 0.05$	---	$0.05 < W$	As round type	2.5
Length	Width	Acceptable Q TY													
---	$W \leq 0.02$	Accept no dense													
$L \leq 3.0$	$0.02 < W \leq 0.03$	2													
$L \leq 2.5$	$0.03 < W \leq 0.05$														
---	$0.05 < W$	As round type													
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction. <table border="1" data-bbox="842 1657 1353 1904"> <thead> <tr> <th>Size Φ</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.20$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.50$</td> <td>3</td> </tr> <tr> <td>$0.50 < \Phi \leq 1.00$</td> <td>2</td> </tr> <tr> <td>$1.00 < \Phi$</td> <td>0</td> </tr> <tr> <td>Total Q TY</td> <td>3</td> </tr> </tbody> </table>	Size Φ	Acceptable Q TY	$\Phi \leq 0.20$	Accept no dense	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$1.00 < \Phi$	0	Total Q TY	3	2.5
Size Φ	Acceptable Q TY														
$\Phi \leq 0.20$	Accept no dense														
$0.20 < \Phi \leq 0.50$	3														
$0.50 < \Phi \leq 1.00$	2														
$1.00 < \Phi$	0														
Total Q TY	3														

NO	Item	Criterion	AQL																		
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination																			
06	Chipped glass	<p>Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length:</p> <p>6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="443 1030 1353 1191"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <p>⊙ If there are 2 or more chips, x is total length of each chip.</p> <p>6.1.2 Corner crack:</p>  <table border="1" data-bbox="443 1568 1353 1729"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <p>⊙ If there are 2 or more chips, x is the total length of each chip.</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	2.5
z: Chip thickness	y: Chip width	x: Chip length																			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$																			
z: Chip thickness	y: Chip width	x: Chip length																			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$																			

NO	Item	Criterion	AQL																
06	Glass crack	<p>Symbols :</p> <p>x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length</p> <p>6.2 Protrusion over terminal :</p> <p>6.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="338 869 1249 952"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq 0.5\text{mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>6.2.2 Non-conductive portion:</p>  <table border="1" data-bbox="408 1276 1249 1359"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged.</p> <p>6.2.3 Substrate protuberance and internal crack.</p>  <table border="1" data-bbox="746 1601 1254 1684"> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td>$y \leq 1/3L$</td> <td>$x \leq a$</td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$x \leq a$	2.5
y: Chip width	x: Chip length	z: Chip thickness																	
$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$																	
y: Chip width	x: Chip length	z: Chip thickness																	
$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$																	
y: width	x: length																		
$y \leq 1/3L$	$x \leq a$																		

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB \ COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  $X * Y \leq 2\text{mm}^2$	2.5 2.5 0.65 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65

14. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.

LCM Sample Estimate Feedback Sheet

Module Number : _____

1 、 Panel Specification :

1. Panel Type :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
2. View Direction :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
3. Numbers of Dots :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
4. View Area :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
5. Active Area :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
6. Operating Temperature :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
7. Storage Temperature :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
8. Others :	_____	

2 、 Mechanical Specification :

1. PCB Size :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
2. Frame Size :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
3. Material of Frame :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
4. Connector Position :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
5. Fix Hole Position :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
6. Backlight Position :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
7. Thickness of PCB :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
8. Height of Frame to PCB :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
9. Height of Module :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
10. Others :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____

3 、 Relative Hole Size :

1. Pitch of Connector :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
2. Hole size of Connector :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
3. Mounting Hole size :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
4. Mounting Hole Type :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
5. Others :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____

4 、 Backlight Specification :

1. B/L Type :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
2. B/L Color :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
3. B/L Driving Voltage (Reference for LED Type) :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
4. B/L Driving Current :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
5. Brightness of B/L :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
6. B/L Solder Method :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
7. Others :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____

>> Go to page 2 <<

Module Number : _____		
5 · <u>Electronic Characteristics of Module</u> :		
1.Input Voltage :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
2.Supply Current :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
3.Driving Voltage for LCD :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
4.Contrast for LCD :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
5.B/L Driving Method :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
6.Negative Voltage Output :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
7.Interface Function :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
8.LCD Uniformity :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
9.ESD test :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
10.Others :	<input type="checkbox"/> Pass	<input type="checkbox"/> NG , _____
6 · <u>Summary</u> :		
<p style="text-align: right;">Sales signature : _____</p> <p style="text-align: right;">Customer Signature : _____ Date : / /</p>		